IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Snyder et al.

Serial:

09/975,030

Group Art Unit: 2123

Filed:

October 10, 2001

Examiner: Dwin M. Craig

For:

EMULATOR CHIP-BOARD ARCHITECTURE AND INTERFACE

Honorable Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

AMENDMENTS and RESPONSE

Dear Sir:

In response to the non-final Office Action mailed March 29, 2005 in the above captioned Patent Application, Applicants respectfully request the Examiner to enter the following amendments and to consider the following remarks.